

ISSUE SLIP STAPLE AREA (for additional cross references)

PL. DN	INITIALS	ID NO.	DATE
FEE DETERMINATION	7.10		8-23-00
O.I.P.E. CLASSIFIER	11		8-28-00
FORMALITY REVIEW	11/2	553	9-29-00
RESPONSE FORMALITY REVIEW	12/8	610080	2/2/01

INDEX OF CLAIMS

✓ Rejected
 = Allowed
 - (Through numeral) Canceled
 + Restricted
 N Non-elected
 I Interference
 A Appeal
 O Objected

APPLICATION NO.
09/639915

APPLICANTS
Wilson, Joseph T.
Daniel L.
TITLE
Integrated
circuit
cached

ORIGINAL

CLASS

INTERNATIONAL C

☐ TERMINAL
DISCLAIMER

☐ The term of this patent
subsequent to
has been disclaimed.

☐ The term of this patent
not extend beyond the
of U.S. Patent. No.

☐ The terminal
this patent have been

WARNING:
The information disclosed
Possession outside the U.S.

Form PTO-436A
(Rev. 6/99)

Claim	Date
Final Original	
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Claim	Date
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